

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Young-pil Kim et al.

Serial No.: 10/796,672

Filed: March 9, 2004

For: SEMICONDUCTOR DEVICE TEST PATTERNS AND RELATED METHODS
FOR PRECISELY MEASURING LEAKAGE CURRENTS IN SEMICONDUCTOR
CELL TRANSISTORS

Examiner: Matthew C. Landau

Group Art Unit: 2815

Confirmation No.: 3015

Date: May 9, 2007

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

This paper is filed in response to the Statement of Reasons For Allowance provided with the Notice of Allowability of May 2, 2007. The Applicants sincerely appreciate the allowance of all pending claims. Applicants wish to point out that there appears to be two inadvertent typographical errors in the Statement for Reasons of Allowance (which was included in the Office Action of January 23, 2007). In particular, the reasons for allowance regarding Claim 6 state that "the prior art of record, either singularly or in combination, does not disclose or suggest the combination of limitations including a fourth contact plug between the second conductive line and the second probing pad that electrically connects the first bit line and the first probing pad." Applicants believe that the Examiner's intention was to state that "the prior art of record, either singularly or in combination, does not disclose or suggest the combination of limitations including a fourth contact plug between the second conductive line and the second probing pad that electrically connects the ~~first~~ second bit line and the ~~first~~ second probing pad." Applicants also point out that the scope and patentability of each claim is defined with respect to the recitations thereof.

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Respectfully submitted,

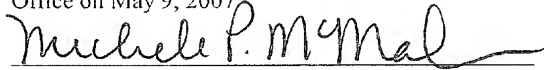


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CERTIFICATION OF ELECTRONIC TRANSMISSION UNDER 37 CFR § 1.8

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Michele P. McMahan

Date of Signature: May 9, 2007